

## EAST Search History

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
S1	0	kasumi near3 (circuit or dsp)	USPAT; USOCR; EPO	OR	OFF	2006/06/25 14:14
S2	185	kasumi	USPAT; USOCR; EPO	OR	OFF	2005/12/12 15:57
S3	48	(S2) and (circuit or dsp)	USPAT; USOCR; EPO	OR	OFF	2005/11/22 16:22
S4	0	(S2) with (circuit or dsp)	USPAT; USOCR; EPO	OR	OFF	2005/11/22 13:30
S5	1	(S2) same (circuit or dsp)	USPAT; USOCR; EPO	OR	OFF	2005/11/22 13:30
S6	1	(S3) and encryption	USPAT; USOCR; EPO	OR	OFF	2005/11/22 15:57
S7	49833	mitsubishi.as.	USPAT; USOCR; EPO	OR	OFF	2005/11/22 13:47
S8	3	(S7) and (kasumi)	USPAT; USOCR; EPO	OR	OFF	2005/11/22 13:47
S9	0	(S7) and (encyrption)	USPAT; USOCR; EPO	OR	OFF	2005/11/22 13:47
S10	91	(S7) and (encryption)	USPAT; USOCR; EPO	OR	OFF	2005/11/22 13:47
S11	29	(S10) and (circuit)	USPAT; USOCR; EPO	OR	OFF	2005/11/22 13:47
S12	4	(S11) and (kasumi or misty)	USPAT; USOCR; EPO	OR	OFF	2005/11/22 13:47
S13	1	kasumi near3 encryption	USPAT; USOCR; EPO	OR	OFF	2005/11/22 17:47
S14	6	kasumi near3 encryption	US-PGPUB	OR	OFF	2005/11/22 15:58
S15	1	3gpp near3 circuit	USPAT; USOCR; EPO	OR	OFF	2005/11/22 17:01

## EAST Search History

S16	1	("20050238166").PN.	US-PGPUB; USPAT; USOCR	OR	OFF	2005/11/22 17:18
S17	0	((feistel near3 algorithm) or (misty)) near3 ((hardware or circuit or fpga or vlsi) near3 implementation)	USPAT; USOCR; EPO	OR	OFF	2005/11/22 17:19
S18	0	((feistel near3 algorithm) or (misty)) near3 (hardware or circuit or fpga or vlsi)	USPAT; USOCR; EPO	OR	OFF	2005/11/22 17:19
S19	4	((feistel near3 algorithm) or (misty)) with (hardware or circuit or fpga or vlsi)	USPAT; USOCR; EPO	OR	OFF	2005/11/22 17:20
S20	8	((feistel) or (misty)) with (hardware or circuit or fpga or vlsi)	USPAT; USOCR; EPO	OR	OFF	2006/12/14 14:25
S21	0	misty near3 encryption near3 hardware	USPAT; USOCR; EPO	OR	OFF	2005/11/22 17:21
S22	49833	mitsubishi.as.	USPAT; USOCR; EPO	OR	OFF	2005/11/22 17:21
S23	13	(S22) and misty	USPAT; USOCR; EPO	OR	OFF	2005/11/22 17:22
S24	5	(S23) and (hardware circuit fpga vlsi)	USPAT; USOCR; EPO	OR	OFF	2006/12/14 14:26
S25	0	kasumi near3 round near3 circuit	USPAT; USOCR; EPO	OR	OFF	2005/11/22 17:47
S26	0	kasumi near3 round near3 circuit	US-PGPUB; USPAT; USOCR; EPO	OR	OFF	2005/11/22 17:47
S27	6	kasumi and round and circuit	US-PGPUB; USPAT; USOCR; EPO	OR	OFF	2005/11/22 17:47
S28	185	kasumi	USPAT; USOCR; EPO	OR	OFF	2005/12/12 15:57
S29	2	(S28) and (hardware near3 implementation)	USPAT; USOCR; EPO	OR	OFF	2006/12/14 14:27
S30	0	(misty near3 hardware near3 implementation)	USPAT; USOCR; EPO	OR	OFF	2005/12/12 16:00

## EAST Search History

S31	0	(misty) with (hardware near3 implementation)	USPAT; USOCR; EPO	OR	OFF	2005/12/12 16:00
S32	0	(misty) same (hardware near3 implementation)	USPAT; USOCR; EPO	OR	OFF	2005/12/12 16:00
S33	5	(misty) and (hardware near3 implementation)	USPAT; USOCR; EPO	OR	OFF	2005/12/12 16:00
S34	45	DES near3 (hardware near3 implement\$6)	USPAT; USOCR; EPO	OR	OFF	2006/12/14 14:27
S35	1	(DES near3 (hardware near3 implement\$6)).ab.	USPAT; USOCR; EPO	OR	OFF	2006/04/19 11:22
S36	15	(S34) and (mux or multiplexer\$1)	USPAT; USOCR; EPO	OR	OFF	2006/04/19 12:49
S37	9038	DES and (circuit near3 diagram)	USPAT; USOCR; EPO	OR	OFF	2006/04/19 12:50
S38	281	(S37) and (mux or multiplexer\$1)	USPAT; USOCR; EPO	OR	OFF	2006/04/19 12:50
S39	7	(S38) and (hardware near3 implementation)	USPAT; USOCR; EPO	OR	OFF	2006/04/19 12:52
S40	16	DES and ((mux or multiplexer) near3 feedback)	USPAT; USOCR; EPO	OR	OFF	2006/12/14 14:29
S41	0	(calculation near3 controller) near3 (crypto\$6 near3 circuit)	USPAT; USOCR; EPO	OR	OFF	2006/06/25 14:15
S42	0	(calculation near3 controller) near3 (encrypt\$6 near3 circuit)	USPAT; USOCR; EPO	OR	OFF	2006/06/25 14:15
S43	1775	(calculation near3 controller)	USPAT; USOCR; EPO	OR	OFF	2006/06/25 14:15
S44	0	(S43) and (crypto\$6 near3 circuit\$2)	USPAT; USOCR; EPO	OR	OFF	2006/06/25 14:16
S45	117	(crypto\$6 near3 circuit\$2)	USPAT; USOCR; EPO	OR	OFF	2006/06/25 14:16

## EAST Search History

S46	1675	((encrypt\$6 or decrypt\$6) near3 circuit\$2)	USPAT; USOCR; EPO	OR	OFF	2006/06/25 14:16
S47	1724	((encrypt\$6 or decrypt\$6 or crypt\$6) near3 circuit\$2)	USPAT; USOCR; EPO	OR	OFF	2006/06/25 14:16
S48	88	(S47) and (calculat\$6 near3 control\$4)	USPAT; USOCR; EPO	OR	OFF	2006/06/25 14:17
S49	0	(S48) and ((sub-key or subkey) near3 generat\$4)	USPAT; USOCR; EPO	OR	OFF	2006/06/25 14:17
S50	39	(S48) and ((sub-key or subkey))	USPAT; USOCR; EPO	OR	OFF	2006/06/25 14:19
S51	39	(S50) and (intermediate near3 value)	USPAT; USOCR; EPO	OR	OFF	2006/06/25 14:19
S52	38	(S50) and (selector)	USPAT; USOCR; EPO	OR	OFF	2006/06/25 14:22
S53	0	(S50) and (partial near3 round near3 calculator)	USPAT; USOCR; EPO	OR	OFF	2006/06/25 14:20
S54	0	(S50) and (partial near3 round)	USPAT; USOCR; EPO	OR	OFF	2006/06/25 14:20
S55	1775	calculation near3 controller	USPAT; USOCR; EPO	OR	OFF	2006/06/25 14:22
S56	3	(S55) and (S47)	USPAT; USOCR; EPO	OR	OFF	2006/06/25 14:27
S57	40	controller near3 ((encrypt\$6 or decrypt\$6 or crypt\$6) near3 circuit)	USPAT; USOCR; EPO	OR	OFF	2006/06/25 14:28
S58	14	(S57) and (control near3 signal)	USPAT; USOCR; EPO	OR	OFF	2006/06/25 14:28
S59	23	controller near3 ((encrypt\$6 or decrypt\$6 or crypt\$6) adj circuit)	USPAT; USOCR; EPO	OR	OFF	2006/06/25 14:32
S60	3	(calculation near3 controller) and ((encrypt\$6 or decrypt\$6 or crypt\$6) adj circuit)	USPAT; USOCR; EPO	OR	OFF	2006/06/25 14:33

## EAST Search History

S61	23	(calculation near3 controller) and ((encrypt\$6 or decrypt\$6 or crypt\$6))	USPAT; USOCR; EPO	OR	OFF	2006/12/14 15:33
S62	0	((sub-key or subkey) near3 generator)	USPAT; USOCR; EPO	OR	OFF	2006/06/25 14:34
S63	52	((sub-key or subkey) near3 generat\$6)	USPAT; USOCR; EPO	OR	OFF	2006/12/14 15:31
S64	0	(S63) and (calculation near3 controller)	USPAT; USOCR; EPO	OR	OFF	2006/06/25 14:35
S65	21	(S63) and (controller)	USPAT; USOCR; EPO	OR	OFF	2006/12/14 15:33
S66	17	(S63) and (calculation)	USPAT; USOCR; EPO	OR	OFF	2006/12/14 15:33
S67	0	controller near3 sequenc\$6 near3 ((encrypt\$6 or decrypt\$6 or crypt\$6) adj circuit)	USPAT; USOCR; EPO	OR	OFF	2006/06/25 14:42
S68	0	control\$4 near3 sequenc\$6 near3 ((encrypt\$6 or decrypt\$6 or crypt\$6) adj circuit)	USPAT; USOCR; EPO	OR	OFF	2006/06/25 14:42
S69	41	control\$4 near3 sequenc\$6 near3 (encrypt\$6 or decrypt\$6 or crypt\$6)	USPAT; USOCR; EPO	OR	OFF	2006/06/25 14:43
S70	4	control\$4 near3 sequenc\$6 near3 ((encrypt\$6 or decrypt\$6 or crypt\$6) near5 circuit)	USPAT; USOCR; EPO	OR	OFF	2006/06/25 14:45
S71	41	control\$4 near3 sequenc\$6 near3 (encrypt\$6 or decrypt\$6 or crypt\$6)	USPAT; USOCR; EPO	OR	OFF	2006/06/25 14:47
S72	0	(S71) and (sub-key or subkey)	USPAT; USOCR; EPO	OR	OFF	2006/06/25 14:45
S73	2	controller near3 input near3 ((encrypt\$6 or decrypt\$6 or crypt\$6) near3 circuit)	USPAT; USOCR; EPO	OR	OFF	2006/06/25 14:49
S74	22	sequenc\$6 near3 ((encrypt\$6 or decrypt\$6 or crypt\$6) near3 circuit)	USPAT; USOCR; EPO	OR	OFF	2006/06/25 14:50
S75	1034	sequencing adj circuit	USPAT; USOCR; EPO	OR	OFF	2006/06/25 14:51

## EAST Search History

S76	0	sequencing adj (round adj circuit)	USPAT; USOCR; EPO	OR	OFF	2006/06/25 14:51
S77	0	sequencing adj (encryption adj circuit)	USPAT; USOCR; EPO	OR	OFF	2006/06/25 14:51
S78	240	(S75) and (controller)	USPAT; USOCR; EPO	OR	OFF	2006/06/25 14:51
S79	2	(S78) and (encrypt\$6 or decrypt\$6 or crypt\$6)	USPAT; USOCR; EPO	OR	OFF	2006/06/25 14:53
S80	6	control\$4 adj ((encrypt\$6 or decrypt\$6 or crypt\$6) adj circuit)	USPAT; USOCR; EPO	OR	OFF	2006/06/25 14:56
S81	58	control\$4 near3 ((encrypt\$6 or decrypt\$6 or crypt\$6) adj circuit)	USPAT; USOCR; EPO	OR	OFF	2006/06/25 15:17
S82	31	(S81) and sequenc\$6	USPAT; USOCR; EPO	OR	OFF	2006/06/25 14:57
S83	1	(S82) and (sub-key or subkey)	USPAT; USOCR; EPO	OR	OFF	2006/06/25 14:57
S84	40	control\$4 near3 ((encrypt\$6 or crypt\$6) adj circuit)	USPAT; USOCR; EPO	OR	OFF	2006/06/25 15:17
S85	0	(S84) and (sub-key or subkey)	USPAT; USOCR; EPO	OR	OFF	2006/06/25 15:17
S86	1	(S84) and (intermediate near3 value)	USPAT; USOCR; EPO	OR	OFF	2006/06/25 15:18
S87	1299	DES near3 circuit	USPAT; USOCR; EPO	OR	OFF	2006/06/25 15:18
S88	5	DES near3 circuit near3 controller	USPAT; USOCR; EPO	OR	OFF	2006/12/14 15:32
S89	3	control\$4 near3 (des adj circuit)	USPAT; USOCR; EPO	OR	OFF	2006/12/14 15:32
S90	92	control\$4 near3 (des near3 circuit)	USPAT; USOCR; EPO	OR	OFF	2006/06/25 15:20

## EAST Search History

S91	0	(S90) and (sub-key or subkey)	USPAT; USOCR; EPO	OR	OFF	2006/06/25 15:21
S92	0	des and (sub-key or subkey) and (control\$4 near3 encrypt\$4 near3 circuit)	USPAT; USOCR; EPO	OR	OFF	2006/06/25 15:22
S93	131	des and (sub-key or subkey)	USPAT; USOCR; EPO	OR	OFF	2006/06/25 15:22
S94	53	(S93) and (intermediate near3 value)	USPAT; USOCR; EPO	OR	OFF	2006/06/25 15:22
S95	41	(S94) and controller	USPAT; USOCR; EPO	OR	OFF	2006/06/25 15:24
S96	41	(S95) and sequenc\$6	USPAT; USOCR; EPO	OR	OFF	2006/06/25 15:22
S97	1299	DES near3 circuit	USPAT; USOCR; EPO	OR	OFF	2006/06/25 15:24
S98	7	(S97) and (encrypt\$6 near3 controller)	USPAT; USOCR; EPO	OR	OFF	2006/06/25 15:25
S99	144	(S97) and (controller)	USPAT; USOCR; EPO	OR	OFF	2006/06/25 15:25
S10 0	1	(S99) and (sub-key or subkey)	USPAT; USOCR; EPO	OR	OFF	2006/06/25 15:27
S10 1	131	DES and (sub-key or subkey)	USPAT; USOCR; EPO	OR	OFF	2006/06/25 15:27
S10 2	53	(S101) and (intermediate near3 value)	USPAT; USOCR; EPO	OR	OFF	2006/06/25 15:29
S10 3	41	(S102) and controller	USPAT; USOCR; EPO	OR	OFF	2006/06/25 15:28
S10 4	41	(S103) and circuit	USPAT; USOCR; EPO	OR	OFF	2006/06/25 15:28
S10 5	60	des adj circuit	USPAT; USOCR; EPO	OR	OFF	2006/12/14 15:32

## EAST Search History

S10 6	15	(S105) and (controller)	USPAT; USOCR; EPO	OR	OFF	2006/06/25 15:30
S10 7	45	encrypt\$6 adj controller	USPAT; USOCR; EPO	OR	OFF	2006/06/25 15:33
S10 8	0	(S107) and (sub-key or subkey)	USPAT; USOCR; EPO	OR	OFF	2006/06/25 15:31
S10 9	584	control\$4 adj encrypt\$6	USPAT; USOCR; EPO	OR	OFF	2006/06/25 15:33
S11 0	4	(S109) and (sub-key or subkey)	USPAT; USOCR; EPO	OR	OFF	2006/06/25 15:34
S11 1	4	(S109) and (intermediate near3 value)	USPAT; USOCR; EPO	OR	OFF	2006/06/25 15:34
S11 2	27	((feistel) or (misty)) with (hardware or circuit or fpga or vlsi)	US-PGPUB; USPAT; USOCR; EPO; JPO	OR	OFF	2006/12/14 14:25
S11 3	52137	mitsubishi.as.	USPAT; USOCR; EPO	OR	OFF	2006/12/14 14:26
S11 4	15	(S113) and misty	USPAT; USOCR; EPO	OR	OFF	2006/12/14 14:26
S11 5	6	(S114) and (hardware circuit fpga vlsi)	US-PGPUB; USPAT; USOCR; EPO; JPO	OR	OFF	2006/12/14 14:26
S11 6	213	kasumi	USPAT; USOCR; EPO	OR	OFF	2006/12/14 14:27
S11 7	3	(S116) and (hardware near3 implementation)	US-PGPUB; USPAT; USOCR; EPO; JPO	OR	OFF	2006/12/14 14:27
S11 8	89	DES near3 (hardware near3 implement\$6)	US-PGPUB; USPAT; USOCR; EPO; JPO	OR	OFF	2006/12/14 14:28
S11 9	16	DES and ((mux or multiplexer) near3 feedback)	US-PGPUB; USPAT; USOCR; EPO; JPO	OR	OFF	2006/12/14 14:30



## EAST Search History

S12 0	117	((sub-key or subkey) near3 generat\$6)	US-PGPUB; USPAT; USOCR; EPO; JPO	OR	OFF	2006/12/14 15:31
S12 1	70	des adj circuit	US-PGPUB; USPAT; USOCR; EPO; JPO	OR	OFF	2006/12/14 15:32
S12 2	5	DES near3 circuit near3 controller	US-PGPUB; USPAT; USOCR; EPO; JPO	OR	OFF	2006/12/14 15:32
S12 3	10	kasumi near3 encryption	US-PGPUB; USPAT; USOCR; EPO; JPO	OR	OFF	2006/12/14 15:32
S12 4	4	control\$4 near3 (des adj circuit)	US-PGPUB; USPAT; USOCR; EPO; JPO	OR	OFF	2006/12/14 15:32
S12 5	66	(calculation near3 controller) and ((encrypt\$6 or decrypt\$6 or crypt\$6))	US-PGPUB; USPAT; USOCR; EPO; JPO	OR	OFF	2006/12/14 15:33
S12 6	57	((sub-key or subkey) near3 generat\$6)	USPAT; USOCR; EPO	OR	OFF	2006/12/14 15:33
S12 7	22	(S126) and (controller)	US-PGPUB; USPAT; USOCR; EPO; JPO	OR	OFF	2006/12/14 15:33
S12 8	19	(S126) and (calculation)	US-PGPUB; USPAT; USOCR; EPO; JPO	OR	OFF	2006/12/14 15:33